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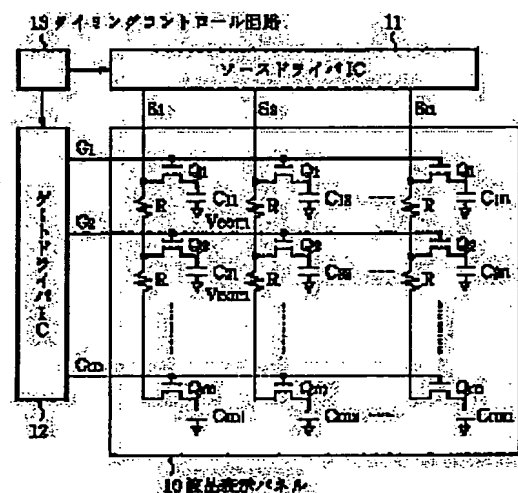
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(54) LIQUID CRYSTAL DISPLAY DEVICE AND ITS DRIVE METHOD

(57)Abstract

PROBLEM TO BE SOLVED: To secure a sufficient charge neutralization time and to reduce power consumption in a source driver IC by using a gate driver capable of simultaneously driving plural gate lines and neutralizing liquid crystal charges on plural horizontal lines.

SOLUTION: The data of a high level are read in an initial stage SR1 of a shift register 21 in the gate driver IC 12, and both output terminals of the gate lines G1, G2 output the high level through OR circuits OR1, OR2. Then, write-in into liquid crystal capacity C11-C1n, C21-C2n becomes possible. Thereafter, all output terminals of the source lines S1-Sn of the source driver IC 11 output Vcom potential by a control signal of a timing control circuit 13, and perform electric neutralization of all liquid crystal capacity in two horizontal lines much. Thereafter, one pulse of CLK is inputted to the gate driver IC 12, and the data of the high level in the shift register 21 are shifted to SR2, and the high level is outputted to only the gate line G1 output terminal.



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